All Optical EX-OR Gate Based On Four Wave Mixing Non Linear Effect in Semiconductor Optical amplifier (SOA)

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Abstract — A novel approach towards the realization of all optical Ex-OR gate using all optical NOR gate, based on Semiconductor optical amplifier (SOA), has been presented in this paper, exploiting Four wave mixing (FWM), non linear effect in SOA’s.

The design and simulation of the circuit has been described with the help of Optiwave software version 7.0 and have been optimized by measuring the output at different values of input parameters like input optical power, injection current and active region length of SOA. Here, in this paper the optimized output has been presented. Several approaches have already been suggested for designing all optical logic gates which includes cross gain modulation (XGM), cross phase modulation (XPM) and Machzehender interferometer (MZI) structure etc. In this paper we will use the idea to generate FWM signals by keeping two data pumps (A and b) frequencies fixed at 193 Thz and 193.1 Thz respectively, which will generate the FWM at 193.2 Thz.

Index Terms — Four wave mixing (FWM), Cross gain modulation (XGM), cross phase modulation (XPM), Semiconductor optical amplifier (SOA), variable optical attenuator (VOA), Band pass filter (BPF), EDFA and Fork (1x2)

1. INTRODUCTION

A non linear device called as semiconductor optical amplifier (SOA), emerged as a practical solution for all-optical signal processing function, where one signal has been controlled by the other signal.

SOA based optical signal processing devices can easily handle complex signal and process the data rates of upto 40Gb/s with much less power consumption of (1W), however modern high speed electronic equipment consumes much more power than 10kw and requires equivalent power to remove the heat generated by the equipment. Also all optical SOA devices can readily be integrated to form an array of devices with smaller footprints. Such kind of SOA based optical signal processing founds vast application in Telecommunication field. Especially, tasks such as wavelength conversion and signal regeneration are remarkable in telecom network.

All-optical signal processing devices is not a replacement of the electronic system, it just increases the effectiveness, capacity and flexibility of the next generation system in optoelectronics domain. In this paper, design of all-optical Ex-or gate using optical NOR gate has been simulated and analyzed, using a non-linear behavior called four wave mixing (FWM) in SOA.

FWM occurs when light of three different wavelengths is launched into a fiber, giving rise to a new wave (known as an idler), the wavelength of which does not coincide with any of the others. FWM is a kind of optical parametric oscillation.

Above, figure shows four-wave mixing in the frequency domain. As can be seen, the light that was there from before launching, sandwiching the two pumping waves in the frequency domain, is called the probe light (or signal light). The idler frequency ‘idler’ may then be determined by the equation:

This paper is broadly divided into three
sections. The first section shows the introduction about all-optical devices and SOA and its non-linear effects (FWM). Section 2 describes the operation principle behind the design of all-optical EXOR gate and section 3 describes the simulation and its results.

2. OPERATION PRINCIPLE

Block Diagram Of All-Optical EXOR GATE using Optical NOR GATE

In this design of optical EXOR gate using optical NOR gate, two data pumps (A & B) same as in NOR gate which carries data signal and five probe pumps (CW Laser) are used to generate the logic of XOR gate. Besides that two Forks (1x2) are used to connect the two data pumps simultaneously to the couplers as shown in the above block diagram. At each stage the intermediate inputs are coupled using a coupler to generate the logic of OR gate and then it is coupled with the probe pump (CW laser) to give the logic of NOR gate. The whole circuit comprises of five Optical NOR gates and if connected as shown in the above figure will give the output of XOR gate, i.e. the output will be high if and only if only one of the input is high otherwise low.

The frequencies of data pumps and probe pump are taken as below. SOA injection current, transmitter input power and active region length of SOA are taken as variable in the design to get the optimum result, but for frequency domain analysis it has been fixed to describe the working principle i.e. FWM and to present the optimized results in this paper. The values taken are as follows:

<table>
<thead>
<tr>
<th>S.No.</th>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>fA</td>
<td>193THz</td>
</tr>
<tr>
<td>2</td>
<td>FB</td>
<td>193.1THz</td>
</tr>
<tr>
<td>3</td>
<td>Fcw laser</td>
<td>192.9THz</td>
</tr>
</tbody>
</table>

Table 1: Input Parameters

Table 2: SOA Parameters

3. SIMULATION AND RESULTS

3.1 Simulation:-

Circuit of optical XOR gate designed on Optiwave software is shown in figure. The transmitter A and B is designed by combining the bit sequence generator and optical Gaussian pulse generator. The bit sequence can be given as input but here only one bit has been used to generate the logic. Optical Gaussian pulse generator generates the optical pulse corresponding to the given input bit of desired frequency and optical power.

Simulation of EXOR gate on Optisystem software is done and is analyzed in the frequency domain by optical spectrum analyzer and in time domain by optical time domain analyzer, power meter is also used to measure the power at the output of every component. As we have already seen in the description of optical NOR gate that it is easy to describe the circuit operation and working principle by analyzing the circuit in frequency domain. And the generated logic can be obtained by measuring the power at the output, and optical time domain analysis is also important to see the shape of input and output signals.

3.2 Results:-

To verify the logic of EXOR gate which says the output will be high if and only if only one of the input is high otherwise low, the values of input optical power, injection current, and active region length of SOA are fixed to give the
optimized results. When any of the inputs are high "1", FWM is not generated at output of SOA as shown in figure and the whole power of CW laser is appeared at the output of the SOA and the output is considered to be high(logic 1). Otherwise low.

The outputs are analyzed in two domains, these are frequency domain and time domain. Optical Spectrum analyzer has been used for frequency domain analysis and Optical time domain visualizer is used for time domain analysis. Also optical power meter is used to check the output power levels.

The results obtained are as follows:

Fig 1: Spectrum analyzer at the output of SOA (No FWM generation) when either \(a=0, b=1\) or \(a=1, b=0\)

Fig 2: Spectrum analyzer at the output of SOA (FWM generation) when either \(a=1, b=1\)

Fig 3: Output of time domain visualizer and power meter at \(A=0\) and \(B=0\)

Fig 4: Output of time domain visualizer and power meter at \(A=0, B=1\)
Truth table obtained from the time domain analysis and power meter outputs corresponding to input signals are as below.

<table>
<thead>
<tr>
<th>S.No.</th>
<th>A</th>
<th>B</th>
<th>Output Power (W)</th>
<th>Output power (dbm)</th>
<th>Logic</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>138.459e-3</td>
<td>21.413</td>
<td>0</td>
</tr>
<tr>
<td>2</td>
<td>0</td>
<td>1</td>
<td>172.173e-3</td>
<td>22.360</td>
<td>1</td>
</tr>
<tr>
<td>3</td>
<td>1</td>
<td>0</td>
<td>182.485e-3</td>
<td>22.612</td>
<td>1</td>
</tr>
<tr>
<td>4</td>
<td>1</td>
<td>1</td>
<td>154.930e-3</td>
<td>21.901</td>
<td>0</td>
</tr>
</tbody>
</table>

Table 3 : Truth table for optical EXOR gate

From the above truth table, it is clear that the output is high only when one of the input is high, otherwise low, which follows the logic of XOR gate.

4. CONCLUSION

Simulation of All-optical EXOR gate using optical NOR gate has been successfully performed using Four wave mixing (FWM) property of SOA and the logic has been verified by checking the power levels at the output of SOA, for all the possible input combinations. The great advantage of SOA gates is that they can be integrated to form gate arrays.

Since the optical gates are the basic elements for designing any combinational and sequential circuits. So this All optical EXOR gate design along with all other optical logic gates can be utilized to design the other circuits for switching and memory devices used in the next generation optical networks.

5. ACKNOWLEDGEMENT

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6. REFERENCES


